

Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	Not Yet Assigned
				Filing Date	Concurrently Herewith
				First Named Inventor	Katherine L. Saenger et al.
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	YOR920030376US1 CBLH/20140-00312-US1

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
AKS	AA	US-6,188,120	02-13-2001	Andricacos, et al.	
AKS	AB	US-6,391,773	05-21-2002	Andricacos, et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> -Number <sup>2</sup> -Kind Code <sup>4</sup> (if known)			

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
AKS	CA	MISRA, ET AL., "Electrical properties of Ru-based alloy gate electrodes for dual metal gate Si-CMOS", <i>IEEE Electron Device Letters</i> 23 354 (2002)		
AKS	CB	ZHONG, ET AL., "Properties of Ru-Ta Alloys as Gate Electrodes for NMOS and PMOS Silicon Devices". <i>IDEM</i> 01 467 (2001)		

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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature	Ashe Kumar Sankar	Date Considered	4/11/05
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